

NLT Technologies, Ltd.

# **TFT COLOR LCD MODULE**

# NL10276BC20-18C

26cm (10.4 Type) XGA LVDS interface (1port)

# PRELIMINARY DATA SHEET

DOD-PP-1263 (1st edition)

All information is subject to change without notice. Please confirm the sales representative before starting to design your system.

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Examples: Military systems, aircraft control equipment, aerospace equipment, nuclear reactor control systems, medical equipment/devices/systems for life support, etc.

The quality grade of this product is the "Standard" unless otherwise specified in this document.





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#### **1. OUTLINE**

#### 1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL10276BC20-18C is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing board, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

#### **1.2 APPLICATION**

• For industrial use

#### **1.3 FEATURES**

- Adoption of ST-NLT (Super-Transmissive Natural Light TFT)
- Long life LED backlight type
- High luminance
- High contrast
- Low reflection
- Wide viewing angle
- Wide temperature range
- LVDS interface
- Reversible-scan direction
- Selectable 8bit or 6bit digital signals for data of RGB
- Replaceable lamp for backlight
- Acquisition product for UL60950-1/CSA C22.2 No.60950-1-03 (File number: E170632)





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### 2. GENERAL SPECIFICATIONS

Display area	210.432 (H) × 157.824 (V) mm			
Diagonal size of display	26cm (10.4 inches)			
Drive system	a-Si TFT active matrix			
Display color	16,777,216 colors (At 8-bit input, FRC terminal= High)262,144 colors (At 6-bit input, FRC terminal= Low or Open)			
Pixel	1,024 (H) × 768 (V) pixels			
Pixel arrangement	RGB (Red dot, Green dot, Blue dot) vertical stripe			
Dot pitch	$0.0685 (H) \times 0.2055 (V) mm$			
Pixel pitch	0.2055 (H) × 0.2055 (V) mm			
Module size	228.0 (W) × 178.5 (H) × 8.7 (D) mm (typ.)			
Weight	380 g (typ.)			
Contrast ratio	900: 1 (typ.)			
Viewing angle	<ul> <li>At the contrast ratio ≥10:1</li> <li>Horizontal: Right side 80° (typ.), Left side 80° (typ.)</li> <li>Vertical: Up side 80° (typ.), Down side 80° (typ.)</li> </ul>			
Designed viewing direction	<ul> <li>At DPS= Low or Open: Normal scan</li> <li>Viewing direction without image reversal: Up side (12 o'clock)</li> <li>Viewing direction with contrast peak: Down side (6 o'clock)</li> <li>Viewing angle with optimum grayscale (γ=2.2): Normal axis (perpendicular)</li> </ul>			
Polarizer surface	Clear + Antireflection (AR)			
Polarizer pencil-hardness	2H (min.) [by JIS K5600]			
Color gamut	At LCD panel center 40 % (typ.) [against NTSC color space]			
Response time	$Ton + Toff (10\% \leftrightarrow 90\%)$ 18 ms (typ.)			
Luminance	At IL= $50mA/one \ circuit$ (750) cd/m <sup>2</sup> (typ.)			
Signal system	LVDS 1port (Receiver: THC63LVDF84B, THine Electronics Inc. or equivalent) 8bit/6bit digital signals for data of RGB colors, Dot clock (CLK), Data enable (DE)			
Power supply voltage	LCD panel signal processing board: 3.3V			
	LED backlight type:			
Backlight	Replaceable part • Lamp holder set: Type No. 104LHS50Recommended LED driver board (Option) • LED driver board: Type No. 104PW03F • Corresponding wiring harness: Type No. 121CBL03			
Power consumption	At IL= 50mA/ one circuit, Checkered flag pattern			



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### **3. BLOCK DIAGRAM**





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Note3: Backlight in detail



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#### 4. DETAILED SPECIFICATIONS

#### 4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification		Unit
Module size	$228.0 \pm 0.5 \text{ (W)} \times 178.5 \pm 0.5 \text{ (H)} \times 8.7 \pm 0.5 \text{ (D)}$	Note1	mm
Display area	210.432 (H) × 157.824 (V)	Note1	mm
Weight	380 (typ.), 400 (max.)		g

Note1: See "8. OUTLINE DRAWINGS".

#### 4.2 ABSOLUTE MAXIMUM RATINGS

	Paramete	r	Symbol	Rating	Unit	Remarks
Power supply voltage	LCD panel si	ignal processing board	VCC	-0.3 to +4.0	V	
Input voltage	Dis	splay signals Note1	VD	-0.3 to VCC+0.3	v	-
for signals	Fun	ction signals Note2	VF	-0.5 10 VCC+0.5	V	
Backlight	For	ward current	IL	0 to +70	mA	per one circuit
Ι	ncident light ir	Π	150,000	lx	Note3	
	Storage temperature			-30 to +80	°C	-
Operating to	maratura	Front surface	TopF	-30 to +80	°C	Note4
Operating ter	Inperature	Rear surface	TopR	-30 to +80	°C	Note5
				≤ 95	%	$Ta \le 40^{\circ}C$
	Relative humidity Note6			≤ 85	%	$40^{\circ}\mathrm{C} < \mathrm{Ta} \le 50^{\circ}\mathrm{C}$
			RH	≤ 55	%	$50^{\circ}\mathrm{C} < \mathrm{Ta} \le 60^{\circ}\mathrm{C}$
	1,0000			≤ 36	%	$60^{\circ}\mathrm{C} < \mathrm{Ta} \le 70^{\circ}\mathrm{C}$
			≤ 24	%	$70^{\circ}\mathrm{C} < \mathrm{Ta} \le 80^{\circ}\mathrm{C}$	
	Absolute hum Note6	hidity	AH	≤ 70 Note7	g/m <sup>3</sup>	-

Note1: D0+/-, D1+/-, D2+/-, D3+/-, CLK+/-

Note2: DPS, FRC, MSL

Note3: If the product surface (polarizer) is exposed to an ultraviolet ray, the polarizer may discolor (Surface treatment may be damaged.). Use a filter to protect the polarizer from the ultraviolet ray.

Note4: Measured at LCD panel surface (including self-heat)

Note5: Measured at LCD module's rear shield surface (including self-heat)

Note6: No condensation

Note7: Water amount at Ta= 80°C and RH= 24%





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### **4.3 ELECTRICAL CHARACTERISTICS**

4.3.1 LCD panel signal processing board

(Ta= 25°C)									
Parameter		Symbol	min.	typ.	max.	Unit	Remarks		
Power supply voltage		VCC	3.0	3.3	3.6	V	-		
Power supply current		ICC	-	450 Note1	780 Note2	mA	at VCC= 3.3V		
Permissible ripple voltage		VRP	-	-	100	mVp-p	for VCC		
Differential input threshold	High	VTH	-	-	+100	mV	at VCM= 1.2V		
voltage	Low	VTL	-100	-	-	mV	Note3		
Terminating resistance		RT	-	100	-	Ω	-		
Input voltage for	High	VFH	0.7VCC	-	VCC	V	CMOS level		
DPS, FRC and MSL signals	Low	VFL	0	-	0.3VCC	V	CIVIOS IEVEI		
Input current for	High	IFH	-	-	300	μΑ			
DPS, FRC and MSL signals	Low	IFL	-300	-	-	μΑ	-		

Note1: Checkered flag pattern [by EIAJ ED-2522].

Note2: Pattern for maximum current

Note3: Common mode voltage for LVDS receiver



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4.3.2 Backlight lamp

						(Note1, Note2, Note3)
Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward current	IL	-	50	55	mA	per one circuit
		(21.2)	(24.0)	(27.2)		Ta= +25°C at IL= 50mA/One circuit
Forward voltage	VL	(19.0)	-	-	v	Ta= +80°C at IL= 50mA/One circuit
Forward voltage		-	-	(31.8)		Ta= -30°C at IL= 50mA/One circuit
			-	-	(30.1)	

Note1: Please drive with constant current.

Note2: The above specifications are for one LED circuit of the backlight.

Note3: The Luminance uniformity may be changed depending on the current variation between 2 circuits. It is recommended that the current value difference among the circuits be less than 5%.

### 4.3.3 Power supply voltage ripple

This product works if the ripple voltage levels are over the permissible values as the following table, but there might be noise on the display image.

Power supp	ly voltage	Ripple voltage Note1 (Measure at input terminal of power supply)	Unit
VCC	3.3V	≤ 100	mVp-p

Note1: The permissible ripple voltage includes spike noise.

#### 4.3.4 Fuse

Parameter	Fuse		Rating	Fusing current	Remarks	
T arameter	Туре	Supplier	Rating	Tusing current	Remarks	
VCC FCC16202AF		KAMAYA ELECTRIC	2.0 A	4.0 A	Note1	
VCC	TCC10202AB	Co., Ltd.	36 V	4.0 A	note1	

Note1: The power supply's rated current must be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.



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#### 4.4 POWER SUPPLY VOLTAGE SEQUENCE

4.4.1 LCD panel signal processing board



- \* These signals should be measured at the terminal of  $100\Omega$  resistance.
- Note1: If there is a voltage variation (voltage drop) at the rising edge of VCC below 3.0V, there is a possibility that a product does not work due to a protection circuit.
- Note2: Display signals (D0+/-, D1+/-, D2+/-, D3+/-, CLK+/-) and function signals (DPS, FRC, MSL) must be set to Low or High-impedance, except the VALID period (See above sequence diagram), in order to avoid the circuitry damage.
  If some of display and function signals of this product are cut while this product is working,

even if the signal input to it once again, it might not work normally. If a customer stops the display and function signals, VCC also must be shut down.

4.4.2 LED driver board



- Note1: These are the display and function signals for LCD panel signal processing board.
- Note2: The backlight should be turned on within the valid period of display and function signals, in order to avoid unstable data display.



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### 4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

4.5.1 LCD panel signal processing board

#### CN1 socket (LCD module side): FI-SE20P-HFE (Japan Aviation Electronics Industry Limited (JAE)) Adaptable plug: FI-S20S (Japan Aviation Electronics Industry Limited (JAE))

Adaptable plug: FI-S20S (Japan Aviation Electronics Industry Limited (JA							(JAE))		
Pin	No	No. Symbol Signal		Signal Input data signal: 8bit		Input data signal:	Remarks		
1 m	140.	Symbol	Signar	MAPA	MAP B	6bit			
1	А	D3+	Pixel data	R0-R1,G0-G1,B0-B1 R6-R7,G6-G7,B6-B7 -			Note1, Note2		
	В	GND	Ground		-	Ground	Note3		
2	А	D3-	Pixel data	R0-R1,G0-G1,B0-B1 R6-R7,G6-G7,B6-B7					
	В	GND	Ground		-	Ground	Note3		
3	3	DPS	Selection of scan direction	High : Low or Open :	Reverse scan Normal scan		Note4		
4	4	FRC	Selection of the number of colors	Hi	igh	Low or Open	Note1 Note5		
5	5	GND	Ground		Ground		Note3		
6	5	CLK+					Note2		
7	7	CLK-	Pixel clock	Pixel clock					
8	3	GND	Ground	Ground					
9	)	D2+	Pixel data						
1	0	D2-	Fixel uata	B4-B7,DE B2-B5,DE					
1	1	GND	Ground		Ground		Note3		
12	2	D1+	Pixel data	G3-G7,B2-B3	G1-G5,	D0 D1	Note2		
1	3	D1-	Tixel data	05-07,62-65	01-03,	D0-D1	Note2		
14	4	GND	Ground		Ground		Note3		
1:	5	D0+	Pixel data	R2-R7.G2	DO D	5 CO	Note2		
1	6	D0-	Pixel data	K2-K7,G2	R0-R	5,60	Note2		
1	7	GND	Ground	Ground					
1	8	MSL	Selection of LVDS input map	Low	High	Low	Note5		
1	9	VCC	Power supply	Power supply					
2	0	VCC	r ower suppry	Power supply					

Note1: See "4.6 DISPLAY COLORS AND INPUT DATA SIGNALS".

Note2: Twist pair wires with  $100\Omega$  (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note3: All GND and VCC terminals should be used without any non-connected lines.

Note4: See "4.8 SCANNING DIRECTIONS".

Note5: See "4.5.4 Connection between receiver and transmitter for LVDS".

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4.5.2 Backlight lamp

CN2 plug (LCD module side): SM10B-SRSS-TB (J.S.T. Mfg. Co., Ltd.) Adaptable socket: SHR\_10V-S\_B (J.S.T. Mfg. Co., Ltd.)

Adaptable sock	set: S	HR-10V-S, SHR-10V-S-B (J.S.T. N	Mfg. Co., Ltd.)
Pin No.	Symbol	Signal	Remarks
1	A1	Anode1	-
2	K1	Cathode1	-
3	A2	Anode2	-
4	K2	Cathode2	-
5	A3	Anode3	-
6	K3	Cathode3	
7	A4	Anode4	-
8	K4	Cathode4	-
9	A5	Anode5	-
10	K5	Cathode5	-

4.5.3 Positions of plugs and a socket

	CN2	1 Insert direction ← 10	
Rear side			
	CN1	1 Insert direction 20	





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- Note3: Twist pair wires with  $100\Omega$  (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.
- Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep TC4, TC5 and TD6 open to avoid noise problem.





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(3) Input data signal: 6bit



- Note1: Recommended transmitter THC63LVDM83D (THine Electronics Inc.) or equivalent
- Note2: LSB (Least Significant Bit) R0, G0, B0 MSB (Most Significant Bit) R5, G5, B5 Note3: Twist pair wires with  $100\Omega$  (Characteristic impedance) should be used between LCD panel
- signal processing board and LVDS transmitter. Note4: Input signals to TC4, TC5 and TD0-6 are not used inside the product, but do not keep TC4, TC5 and TD0-6open to avoid noise problem.



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- 4.5.5 Input data mapping
- (1) Input data signal: 8bit, MAPA



(2) Input data signal: 8bit, MAP B





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### 4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

4.6.1 Combinations of input data signals, FRC signal and MSL signal

This product can display 16,777,216 colors equivalent with 256 gray scales and 262,144 colors with 64 gray scales by combination of input data signals, FRC and MSL signal. See the following table.

Combination	Input data signals	Input Data mapping	CN1- Pin No.1 and 2	FRC terminal	MSL terminal	Display colors	Remarks
1	8 bit	MAPA	D3+/-	High	Low	16,777,216	Note1
2	8 bit	MAP B	D3+/-	High	High	16,777,216	Note1
3	6 bit	-	GND	Low or Open	Low	262,144	Note2

Note1: See "**4.6.2 16,777,216 colors**". Note2: See "**4.6.3 262,144 colors**".

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#### 4.6.2 16,777,216 colors

This product can display 16,777,216 colors equivalent with 256 gray scales by combination ① or ②. (See "**4.6.1 Combinations of input data signals, FRC and MSL signal**".) Also the relation between display colors and input data signals is as follows.

Display	y colors								Data	a sig	nal	(0: I	Low	leve	el, 1	: Hi	gh le	evel)	)						
Dispitay	01015	R7	7 R6	6 R5	R4	R3	R2	R1	R0	G	7 G6	6 G5	G4	G3	G2	G1	G0	B7	7 B6	5 B5	B4	B3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
ors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Basic Colors	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	-1	1	1	1	1
sic	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Ba	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	$\uparrow$					:								:								:			
l gr	$\downarrow$					:								:								:			
Red	bright	1	1	1	1	1	1	0	1	0	0	0	-0	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
gray	<b>↑</b>					:								:								:			
Green gray scale	$\downarrow$					: (								:								:			
Jree	bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
U	-	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
le		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue gray scale	$\uparrow$					:								:								:			
e 20	$\downarrow$					:								:								:			
Blu	bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

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#### 4.6.3 262,144 colors

This product can display 262,144 colors with 64 gray scales by combination ③. (See "**4.6.1 Combinations of input data signals, FRC and MSL signal**".) Also the relation between display colors and input data signals is as follows.

Display												ligh le							
Display	01013	R 5	R4	R 3	R 2	R 1	R0	G5	G4	G3	G2	G1	G0	B 5	B4	B 3	B 2	B 1	B 0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
ors	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0 <	0	0	0
Basic colors	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
Isic	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
$\mathrm{B}^{\mathfrak{g}}$	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	$\uparrow$				:						:						:		
l gr	$\downarrow$				:						:						:		
Red	bright	1	1	1	1	0	1	0	0	-0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
SC	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green gray scale	1				:						:						:		
g ng	$\downarrow$				:						:						:		
Jree	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
0		0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
sca	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
ay	$\uparrow$		۵ `		:						:						:		
e gi	$\downarrow$				:						:						:		
Blue gray scale	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1





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### 4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel (See "4.8 SCANNING DIRECTIONS".).

C (0,	0)					
R G	В					
K U	D					
1						
$\left(\begin{array}{cc} C(&0,&0) \right)$	C(1, 0)	• • •	C( X, 0)	• • •	C(1022, 0)	C(1023, 0)
$\widetilde{C(0, 1)}$	C(1, 1)	• • •	C( X, 1)	• • •	C(1022, 1)	C(1023, 1)
•	•	•	•	•	•	•
•	•	• • •	•	• • •	•	•••
•	•	•	•	•	•	•
C( 0, Y)	C( 1, Y)	• • •	C( X, Y)	• • •	C(1022, Y)	C(1023, Y)
•	•	•	•	•		•
•	•	• • •	•	• • •	•	•
•	•	•	•	•	•	•
C( 0, 766)	C(1,766)	• • •	C( X, 766)	•••	C(1022, 766)	C(1023, 766)
C( 0, 767)	C(1,767)	• • •	C( X, 767)	•••	C(1022, 767)	C(1023, 767)

#### **4.8 SCANNING DIRECTIONS**

The following figures are seen from a front view.



Note1

Figure1. Normal scan (DPS: Low or Open)



Figure2. Reverse scan (DPS: High)

Note1: Meaning of C(X, Y) and D(X, Y)

C (X, Y): The coordinates of the display position (See "**4.7 DISPLAY POSITIONS**".) D (X, Y): The data number of input signal for LCD panel signal processing board





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#### 4.9 INPUT SIGNAL TIMINGS

- 4.9.1 Outline of input signal timings
  - Horizontal signal

Note1



Note1: This diagram indicates virtual signal for set up to timing. Note2: See "**4.9.3 Input signal timing chart**" for the pulse number.

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4.9.2 Timing characteristics

							(Note	e1, Note2, Note3)		
	Parameter		Symbol	min.	typ.	max.	Unit	Remarks		
	Fre	1/tc	60.0	65.0	68.0	MHz	15.385 ns (typ.)			
CLK	]	Duty	-				-			
	Rise tin	ne, Fall time	-		-		ns	-		
	CLK-DATA	Setup time	-				ns			
DATA	CERDAIA	Hold time		-			ns	-		
	Rise tin	ne, Fall time	-				ns			
		Cycle	th	19.67	20.676	22.4	μs			
	Horizontal	Cycle	tii	-	1,344	-	CLK	48.363 kHz (typ.)		
		Display period	thd	1,024			CLK			
	Vertical	Cycle	tv	13.3	16.666	18.5	ms			
DE	(One frame)	Cycle	ιv	780	806	-	Н	60.0 Hz (typ.)		
	(,	Display period	tvd		768		Н			
	CLK-DE	Setup time	-				ns			
	CER-DE	Hold time	-		-		ns	-		
	Rise tin	ne, Fall time	-		$\frown$		ns			

Note1: Definition of parameters is as follows.

tc = 1CLK, th = 1H

Note2: See the data sheet of LVDS transmitter.

Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).

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### 4.9.3 Input signal timing chart

Horizontal timing





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### 4.10 OPTICS

4.10.1 Optical characteristics

								(Note1,	
Parameter		Condition	Symbol	min.	typ.	max.	Unit	Measuring instrument	Remarks
Luminar	ice	White at center $\theta R = 0^\circ, \ \theta L = 0^\circ, \ \theta U = 0^\circ, \ \theta D = 0^\circ$	L	(480)	(750)	-	cd/m <sup>2</sup>	BM-5A	-
Contrast r	atio	White/Black at center $\theta R=0^\circ, \ \theta L=0^\circ, \ \theta U=0^\circ, \ \theta D=0^\circ$	CR	400	00 900		BM-5A	Note3	
Luminance un	iformity	White $\theta R = 0^\circ, \ \theta L = 0^\circ, \ \theta U = 0^\circ, \ \theta D = 0^\circ$	LU	-	1.25	1.4	-	BM-5A	Note4
	White	<b>x</b> coordinate	Wx	0.263	0.313	0.363	-		
	white	<b>y</b> coordinate	Wy	0.279	0.329	0.379	-		
	Red	<b>x</b> coordinate	Rx	-	0.543	-			
Chromaticity		<b>y</b> coordinate	Ry	-	0.337	-	-		
Cinomatienty	Green	<b>x</b> coordinate	Gx	-	0.357	-	-	SR-3	Note5
	Ultell	<b>y</b> coordinate	Gy	-	0.547	-	_	51(-5	Notes
	Blue	x coordinate	Bx	_	0.156	-	-		
	Diuc	y coordinate	By	-	0.124	-	-		
Color gai	Color gamut $\theta R = 0^{\circ}, \theta L = 0^{\circ}, \theta U = 0^{\circ}, \theta D = at center, against NTSC color s$		С	35	40	-	%		
Response	time	White to Black	Ton	-	3	5	ms	BM-5A	Note6
Response	ume	Black to White	Toff		15	21	ms	DIVI-JA	Note7
	Right	$\theta U=0^{\circ}, \ \theta D=0^{\circ}, \ CR\geq 10$	θR	70	80	-	0		
Viewing on -1-	Left	$\theta U=0^{\circ}, \ \theta D=0^{\circ}, \ CR\geq 10$	θL	70	80	-	0	EZ	Nota
Viewing angle	Up	$\theta R = 0^\circ, \ \theta L = 0^\circ, \ CR \ge 10$	θU	70	80	-	0	Contrast	Note8
	Down	$\theta R = 0^\circ, \ \theta L = 0^\circ, \ CR \ge 10$	θD	70	80	-	0	1	
NT ( 1	701	· · · · · · · · · · · · · · · · · · ·							

Note1: These are initial characteristics.

Note2: Measurement conditions are as follows.

Ta= 25°C, VCC= 3.3V, IL= 50mA/one circuit, Display mode: XGA, Horizontal cycle= 1/48.363kHz, Vertical cycle= 1/60.0Hz, DPS= Low or Open: Normal scan

Optical characteristics are measured at luminance saturation after 20minutes after the product works in the dark room. Also measurement methods are as follows.



- Note3: See "4.10.2 Definition of contrast ratio".
- Note4: See "4.10.3 Definition of luminance uniformity".
- Note5: These coordinates are found on CIE 1931 chromaticity diagram.
- Note6: Product surface temperature: TopF= (28)°C
- Note7: See "4.10.4 Definition of response times".
- Note8: See "4.10.5 Definition of viewing angles".



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4.10.2 Definition of contrast ratio

The contrast ratio is calculated by using the following formula.

Contrast ratio (CR) = Luminance of white screen Luminance of black screen

4.10.3 Definition of luminance uniformity

The luminance uniformity is calculated by using following formula.

 $Luminance uniformity (LU) = \frac{Maximum luminance from (1) to (5)}{Minimum luminance from (1) to (5)}$ 

The luminance is measured at near the 5 points shown below.

	171	512	853	
128	1	   	2	
384		<mark>3</mark>		
640	4		5	

4.10.4 Definition of response times

Response time is measured at the time when the luminance changes from " white " to " black ", or " black " to " white " on the same screen point, by photo-detector. Ton is the time when the luminance changes from 90% down to 10%. Also Toff is the time when the luminance changes from 10% up to 90% (See the following diagram.).



4.10.5 Definition of viewing angles





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### 5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

#### This lifetime is the estimated value, and is not guarantee value.

	Estimated luminance lifetime (Life time expectancy) Note1, Note2, Note3	Unit	
LED elementary substance	25°C (Ambient temperature of the product) Continuous operation, IL= 50mA/one circuit	70,000	h
LED elementary substance	80°C (Surface temperature at screen) Continuous operation, IL= 50mA/one circuit	60,000	h

Note1: Life time expectancy is mean time to half-luminance.

Note2: Estimated luminance lifetime is not the value for an LCD module but the value for LED elementary substance.

Note3: By ambient temperature, the lifetime changes particularly. Especially, in case the product works under high temperature environment, the lifetime becomes short.





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### 6. RELIABILITY TESTS

Test item	Condition	Judgment Note1			
High temperature and humidity (Operation)	<ol> <li>60 ± 2°C, RH= 90%, 240hours Note3, Note4</li> <li>Display data is black.</li> </ol>				
High temperature (Operation)	<ol> <li>80 ± 3°C, 240hours Note3, Note4</li> <li>Display data is black.</li> </ol>				
Heat cycle (Operation)	<ul> <li>(1) -30 ± 3°C1hour 80 ± 3°C1hour Note3, Note4</li> <li>(2) 50cycles, 4 hours/cycle</li> <li>(3) Display data is black.</li> </ul>	No display malfunctions			
Thermal shock (Non operation)	<ol> <li>-30 ± 3°C30minutes 80 ± 3°C30minutes</li> <li>100cycles, 1hour/cycle</li> <li>Temperature transition time is within 5 minutes.</li> </ol>				
ESD (Operation)	<ol> <li>150pF, 150Ω, ±10kV</li> <li>9 places on a panel surface Note2</li> <li>10 times each places at 1 sec interval</li> </ol>				
Dust (Operation)	<ol> <li>Sample dust: No. 15 (by JIS-Z8901)</li> <li>15 seconds stir</li> <li>8 times repeat at 1 hour interval</li> </ol>				
Vibration (Non operation)	<ol> <li>5 to 100Hz, 19.6m/s<sup>2</sup></li> <li>1 minute/cycle</li> <li>X, Y, Z directions</li> <li>120 times each directions</li> </ol>	No display malfunctions – No physical damages			
Mechanical shock (Non operation)	<ol> <li>539m/ s<sup>2</sup>, 11ms</li> <li>±X, ±Y, ±Z directions</li> <li>5 times each directions</li> </ol>				

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.

Note2: See the following figure for discharge points.



Note3: Measured at LCD panel surface (including self-heat)

Note4: Measured at LCD module's rear shield surface (including self-heat)



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#### 7. PRECAUTIONS

### 7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. **Be sure to read ''7.2 CAUTIONS'' and ''7.3 ATTENTIONS''!** 



This sign has the meaning that a customer will be injured or the product will sustain damage if the customer practices wrong operations.



This sign has the meaning that a customer will be injured if the customer practices wrong operations.

#### 7.2 CAUTIONS

\* Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: Equal to or no greater than 539m/s<sup>2</sup> and equal to or no greater than 11ms, Pressure: Equal to or no greater than 19.6 N (\$\phi16mm jig))

7.3.1 Handling of the product

- ① Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
- ② When the product is put on the table temporarily, display surface must be placed downward.
- ③ When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- (4) The torque for product mounting screws must never exceed 0.147 N·m. Higher torque might result in distortion of the bezel. And the length of product mounting screws must be  $\leq 2.0$ mm.
- ⑤ The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
- ⑤ Do not press or rub on the sensitive product surface. When cleaning the product surface, wipe it with a soft dry cloth.
- ⑦ Do not push or pull the interface connectors while the product is working.
- When handling the product, use of an original protection sheet on the product surface (polarizer) is recommended for protection of product surface. Adhesive type protection sheet may change color or characteristics of the polarizer.
- ③ Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal by any chance, please wash it away with soap and water.



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- 7.3.2 Environment
  - ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
  - ② In order to prevent dew condensation occurred by temperature difference, the product packing box must be opened after enough time being left under the environment of an unpacking room. Evaluate the storage time sufficiently because dew condensation is affected by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with the original packing state after a customer receives the package)
  - ③ Do not operate in high magnetic field. If not, circuit boards may be broken.
  - ④ This product is not designed as radiation hardened.

#### 7.3.3 Characteristics

#### The following items are neither defects nor failures.

- ① Characteristics of the LCD (such as response time, luminance, color uniformity and so on) may be changed depending on ambient temperature. If the product is stored under condition of low temperature for a long time, it may cause display mura. In this case, the product should be operated after enough time being left under condition of operating temperature.
- ② Display mura, flickering, vertical streams or tiny spots may be observed depending on display patterns.
- ③ Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
- ④ The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
- ⑤ Optical characteristics may be changed depending on input signal timings.
- (6) The product gives AR (antireflection) coating of the polarizer surface. Though AR (antireflection) coating actualizes the low reflection with the multilayer structure, the color of reflection may differ among products and the color change of reflection may occur in the same product by fluctuation of AR (antireflection) coating.

#### 7.3.4 Other

- ① All GND and VCC terminals should be used without any non-connected lines.
- ② Do not disassemble a product or adjust variable resistors.
- ③ See "REPLACEMENT MANUAL FOR LAMP HOLDER SET", when replacing lamp holder set.
- ④ Pack the product with the original shipping package, in order to avoid any damages during transportation, when returning the product to NLT for repairing and so on.





Unit: mm

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### **REVISION HISTORY**

The inside of latest specifications is revised to the clerical error and the major improvement of previous edition. Only a changed part such as functions, characteristic value and so on that may affect a design of customers, are described especially below.

Edition	Document number	Prepared date		Revision contents and signa	ture
1st	DOD-PP-	Sep. 2,	Revision contents		
edition	1263	2011	New issue		
			Signature of writer		
			Approved by	Checked by	Prepared by
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			T. OGAWA		A. KUMANO