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TITLE : EV190E0M-N50

Product Specification

Rev.0

BEIJING BOE Display TECHNOLOGY

SPEC. NUMBER	PRODUCT GROUP	Rev. 0	ISSUE DATE	PAGE
S	TFT-LCD		2018.10.08	1 OF 30

BOE	PRODUCT GROUP	REV	ISSUE DATE
	TFT- LCD PRODUCT	Rev. 0	2018-10-08

REVISION HISTORY

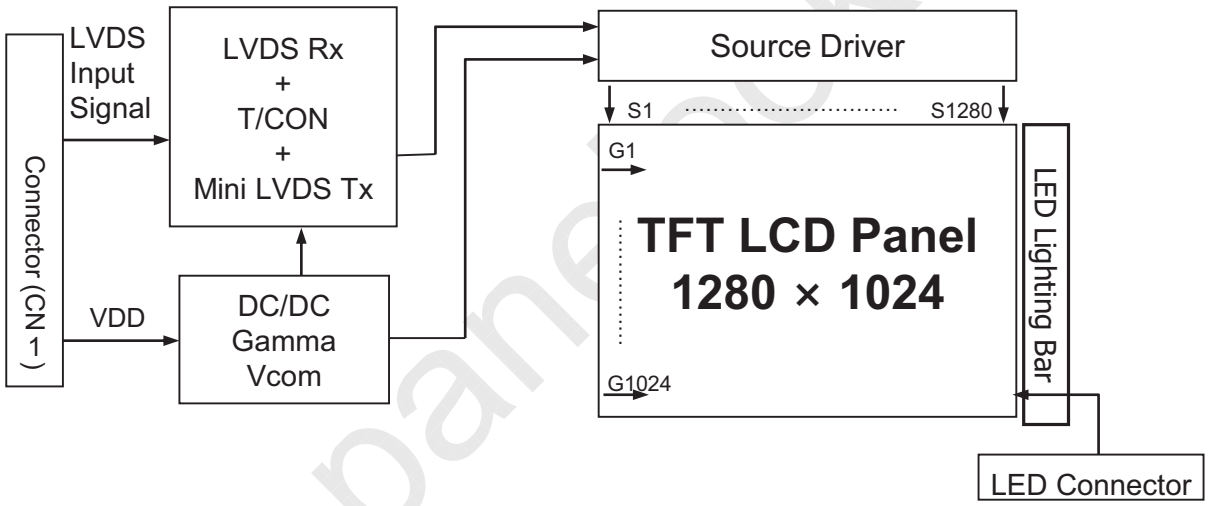
REV.	ECN No.	DESCRIPTION OF CHANGES	DATE	PREPARED
Rev.0		Initial Release	2018-10-08	Bo Liang

Contents

1.0 GENERAL DESCRIPTION

1.1 Introduction

EV190E0M-N50 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 19 inch diagonally measured active area with SXGA resolutions (1280 horizontal by 1024 vertical pixel array). Gray scale or the brightness of the sub-pixel is determined with a 6-bit gray scale signal. The TFT-LCD panel used for this module is adapted for a low reflection, no RGB CF resin, high luminance, contrast ratio and wide viewing angle .



1.2 Features

- LVDS Interface with 2 pixel / clock
- High-speed response
- Incorporated edge type back-light (LED)
- High luminance and contrast ratio, low reflection and wide viewing angle
- DE (Data Enable) only
- RoHS/Halogen Free
- Gamma Correction

1.3 Application

- Desktop Type of PC & Workstation Use
- Slim-Size Display for Stand-alone Monitor
- Display Terminals for Control System
- Monitors for Process Controller

1.4 General Specification

The followings are general specifications at the model EV190E0M-N50.

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	374.784(H) x 299.8272(V)	mm	
Number of pixels	1280(H) ×1024(V)	pixels	
Pixel pitch	0.0976(H) x 0.2928(V)	mm	
Display colors	6bit per1 sub-pixel, grayscale	colors	
Display mode	Normal Black		
Dimensional outline	396.0(H) × 324.0(V)× 9.9(D) typ.	mm	
Weight	1690 (Typ.)	g	
Surface Treatment	Haze 25%, 3H		
Back-light	Right edge side, 1-LED Lighting Bar type		Note 1
Power Consumption	P_D : 5.5 W (max)		
	P_{BL} : 10.56W (max)		Note 2
	P_{total} : 16.39 (max)		

Notes : 1. LED Lighting Bar (4*input pins)

2. P_{LED}=Input pins* VPIN×IPIN

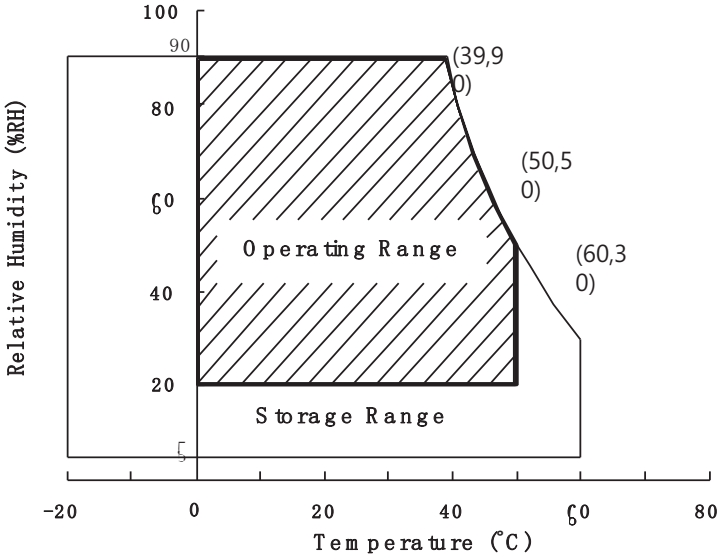
2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings> [VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-0.3	6.0	V	Ta = 25 ℃
Logic Supply Voltage	V_{IN}	VSS-0.3	V_{DD}+0.3	V	
LED Light Bar Current Per Input Pin	IPIN	-	110	mA	
LED Light Bar Voltage Per Input Pin	VPIN	40.5	48	V	
Operating Temperature	T_{OP}	0	+50	℃	1)
Storage Temperature	T_{ST}	-20	+60	℃	1)
Panel Surface Temperature (Operation)	Tsurface	0	+65	℃	2)

Note : 1) Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39 ℃ max. and no condensation of water.



Note : 2) Panel Surface Temperature should be Min. 0 ℃ and Max. +65℃ under the VDD = 5.0V, Frame rate = 60Hz,25 ℃ ambient Temp. no humidity control and LED string current is typical value.

3.0 ELECTRICAL SPECIFICATIONS

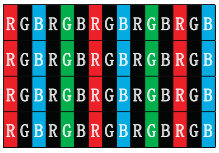
3.1Electrical Specifications

< Table 3. Electrical specifications >

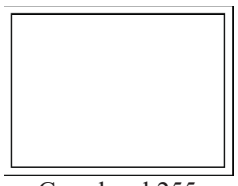
[Ta =25±2 ℃]

Parameter		Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	4.5	5.0	5.5	V	Note1
Power Supply Current	I_{DD}	-	600	1100	mA	
In-Rush Current	I_{RUSH}	-	2.0	3.0	A	Note 2
Permissible Input Ripple Voltage	V_{RF}	-	-	300	mV	Note 4
High Level Differential Input Threshold Voltage	V_{IH}	-	-	+100	mV	
Low Level Differential Input Threshold Voltage	V_{IL}	-100	-	-	mV	
Differential input voltage	$\|V_{ID}\|$	200	-	600	mV	
Differential input common mode voltage	Vcm	1.0	1.2	1.5		V_{IH}=100mV, V_{IL}=-100mV
Power Consumption	P_D	-	3	5.5	W	Note 1
	P_{BL}	8.91	9.74	10.56	W	Note 3
	P_{total}	-	13.23	16.39	W	

Notes : 1. The supply voltage is measured and specified at the interface connector of LCM.
The current draw and power consumption specified is for VDD=5.0V, Frame rate=75Hz
Clock frequency = 92.89 MHz. Test Pattern of power supply current
a) Typ : Checker 32*32
b) Max : Vertial sub line



2. Duration of rush current is about 2 ms and rising time of VDD is 520 μs ± 20 %
3. Calculated value for reference (Input pins*VPIN ×IPIN) excluding inverter loss.
4. Permissible Input ripple Voltage should be measured under V_{DD} =5.0V, 25°C, fV(frame frequency)=MAX condition(@ Gray level 255 Gray level 0) and At that time, we recommend the bandwidth configuration of oscilloscope is to be under 20Mhz. Ripple Voltage should be covered by Input voltage Spec.



Gray level 255

Gray level 0

3.2 Backlight Unit

< Table 4. LED Backlight Unit >

Parameter		Min.	Typ.	Max.	Unit	Remarks
LED Light Bar Input Voltage Per Input Pin	VPIN	40.5	44.3	48	V	Duty 100%
LED Light Bar Input Current Per Input Pin	IPIN	-	110	-	mA	Note1,2,
LED Power Consumption	P_{BL}	8.91	9.74	10.56	W	Note 3
LED Life-Time	-	50,000	-		Hrs	Note 4

Note1: There are one light bar ,and the specified current is input LED chip 100% duty current

Note2: The sense current of each input pin is 110mA

Note3: P_{BL}=2Input pins*VPIN ×IPIN

Note4: The lifetime is determined as the time at which luminance of LED become 50% of the initial brightness or not normal lighting at IPIN=110mA on condition of continuous operating at 25 ±2 ℃

4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = 25±2℃) with the equipment of Luminance meter system (Goniometer system and TOPCONE BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0°. We refer to $\theta_{Ø=0}$ (=θ_3) as the 3 o'clock direction (the "right"), $\theta_{Ø=90}$ (= θ_{12}) as the 12 o'clock direction ("upward"), $\theta_{Ø=180}$ (= θ_9) as the 9 o'clock direction ("left") and $\theta_{Ø=270}$(= θ_6) as the 6 o'clock direction ("bottom"). While scanning θ and/or Ø, the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 5.0V +/-10% at 25°C. Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

[VDD = 5.0V, Frame rate = 60Hz, Clock = 74.25MHz, I_{BL} = 220mA, Ta =25±2 ℃]

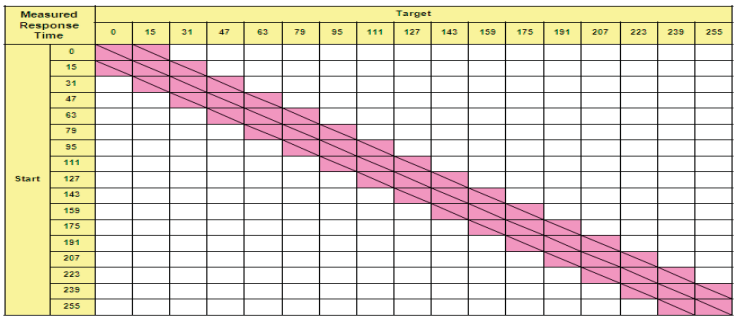
Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle range	Horizontal	Θ_3	CR > 10	85	89	-	Deg.	Note 1
		Θ_9		85	89	-	Deg.	
	Vertical	Θ_{12}		85	89	-	Deg.	
		Θ_6		85	89	-	Deg.	
Luminance Contrast ratio		CR	Θ = 0° (Center) Normal Viewing Angle	1000	1200			Note 2
Luminance of White		Y_w		1100	1300		cd/m²	Note 3
White luminance uniformity		ΔY		75	-		%	Note 4
Reproduction of color	White	W_x		0.235	0.265	0.295	-	Note 5
		W_y		0.275	0.305	0.335	-	
Response Time	GTG	T_g			14	20	ms	Note 6
	Rising	T_r			8	11	ms	
	Falling	T_f			8	11	ms	
Cross Talk		CT		-	-	2.0	%	Note 7

Note :

1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
2. Contrast measurements shall be made at viewing angle of θ= 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIGURE 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. Center Luminance of white is defined as the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
4. The White luminance uniformity on LCD surface is then expressed as :
   ΔY = (Minimum Luminance of 9points / Maximum Luminance of 9points) * 100
   (See FIGURE 2 shown in Appendix).
5. The color chromaticity coordinates specified in Table 4. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
6. Response time T_g is the average time required for display transition by switching the input signal as below table and is based on Frame rate fV =60Hz to optimize.
   Each time in below table is defined as Figure 3 and shall be measured by switching the signal for "any level of gray(bright)"and "any level of gray(dark)". Response time T_r shall be measured by switching the signal from " 0 level of gray" to "255 level of gray" in Figure 3. And response time T_f shall be measured by switching the signal from " 255 level of gray" to "0 level of gray" in Figure 3.



7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark. (See FIGURE 4 shown in Appendix).

5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

5.1.1 LED Light Bar

< Table 1. LED Light Bar>

Pin No	Symbol	Description
1	IRLED1	LED current sense for string1
2	NC	No Connection
3	VLED	LED power supply
4	VLED	LED power supply
5	NC	No Connection
6	IRLED2	LED current sense for string2
7	CONNECTOR	3707K-S06N-00X

Remark: The mating type connector: ENTERY H112K-DXXN-20,22B or equivalent

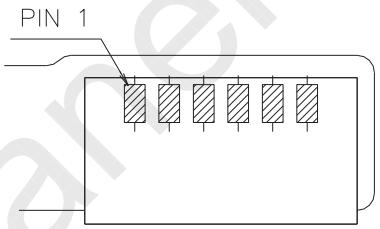


Figure1. Top View of LED Bar Connector

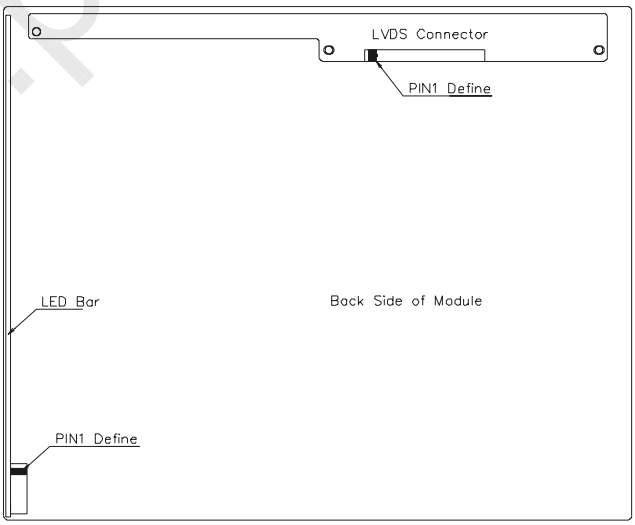


Figure2. Back Side of Module

5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

- CN11 Module Side Connector : UJU IS100-L30R-C23or Equivalent
  User Side Connector : JAE FI-X30H or Equivalent

Pin No	Symbol	Function	Remark
1	RXO0-	Negative Transmission data of Pixel 0 (ODD)	
2	RXO0+	Positive Transmission data of Pixel 0 (ODD)	
3	RXO1-	Negative Transmission data of Pixel 1 (ODD)	
4	RXO1+	Positive Transmission data of Pixel 1 (ODD)	
5	RXO2-	Negative Transmission data of Pixel 2 (ODD)	
6	RXO2+	Positive Transmission data of Pixel 2 (ODD)	
7	GND	Power Ground	
8	RXOC-	Negative Transmission Clock (ODD)	
9	RXOC+	Positive Transmission Clock (ODD)	
10	RXO3-	Negative Transmission data of Pixel 3 (ODD)	
11	RXO3+	Positive Transmission data of Pixel 3 (ODD)	
12	RXE0-	Negative Transmission data of Pixel 0 (EVEN)	
13	RXE0+	Positive Transmission data of Pixel 0 (EVEN)	
14	GND	Power Ground	
15	RXE1-	Negative Transmission data of Pixel 1 (EVEN)	
16	RXE1+	Positive Transmission data of Pixel 1 (EVEN)	
17	GNG	Power Ground	
18	RXE2-	Negative Transmission data of Pixel 2 (EVEN)	
19	RXE2+	Positive Transmission data of Pixel 2 (EVEN)	
20	RXEC-	Negative Transmission Clock (EVEN)	
21	RXEC+	Positive Transmission Clock (EVEN)	
22	RXE3-	Negative Transmission data of Pixel 3 (EVEN)	
23	RXE3+	Positive Transmission data of Pixel 3 (EVEN)	
24	GND	Power Ground	Note 1
25	NC	No. Connection	
26	NC	No. Connection	
27	NC	No. Connection	
28	VDD	Power Supply: +5V	
29	VDD		
30	VDD		

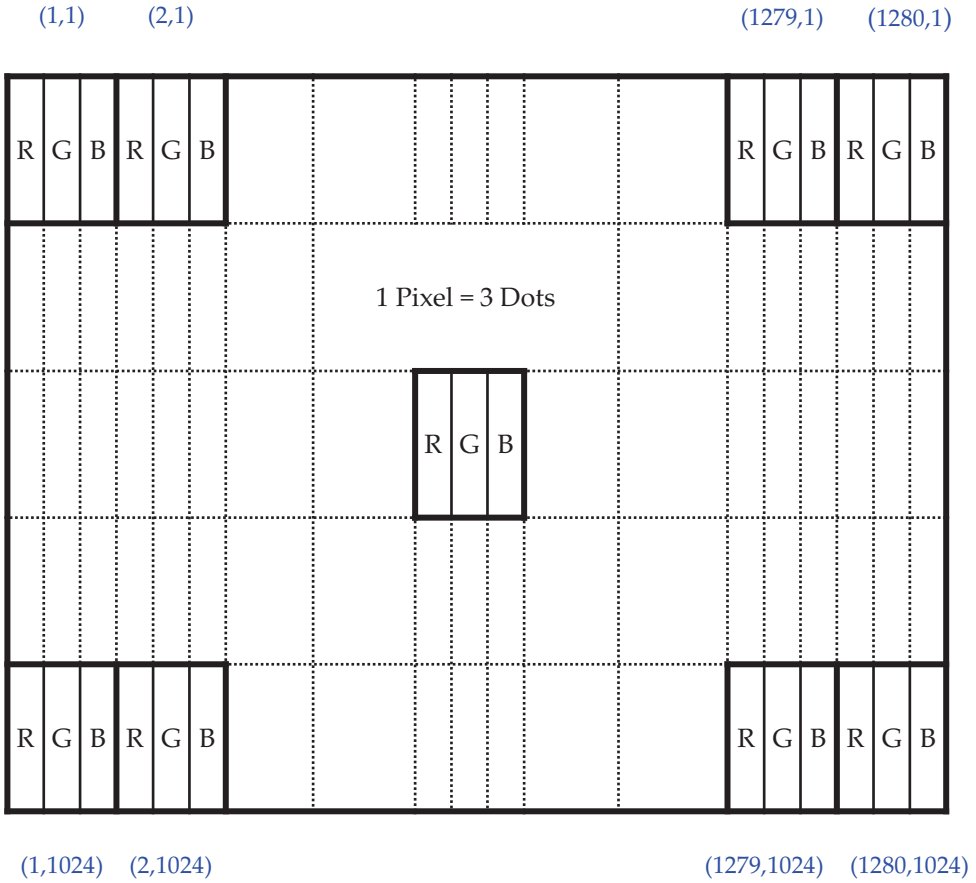
Note 1 : This pin should be connected with GND.

5.2 LVDS Interface (Tx; THC63LVDF83A or Equivalent)

5.2.1 LVDS Interface

	Input Signal	Transmitter		Interface		HT236F01-100 (CN11)	Remark
		Pin No.	Pin No.	System (Tx)	TFT-LCD (Rx)	Pin No.	
LVDS	OR0	51	48 47	OUT0- OUT0+	RXO0- RXO0+	1 2	
	OR1	52					
	OR2	54					
	OR3	55					
	OR4	56					
	OR5	3					
	OG0	4					
	OG1	6	46 45	OUT1- OUT1+	RXO1- RXO1+	3 4	
	OG2	7					
	OG3	11					
	OG4	12					
	OG5	14					
	OB0	15					
	OB1	19					
	OB2	20	42 41	OUT2- OUT2+	RXO2- RXO2+	5 6	
	OB3	22					
	OB4	23					
	OB5	24					
	Hsync	27					
	Vsync	28					
	DE	30					
	MCLK	31	40 39	CLK OUT- CLK OUT+	RXO CLK- RXO CLK+	8 9	
	OR6	50	38 37	OUT3- OUT3+	RXO3- RXO3+	10 11	
	OR7	2					
	OG6	8					
	OG7	10					
	OB6	16					
	OB7	18					
	RSVD	25					

5.3 Data Input Format



Display Position of Input Data (V-H)

6.0 SIGNAL TIMING SPECIFICATION

6.1 The EV190E0M-N50 is operated by the DE only.

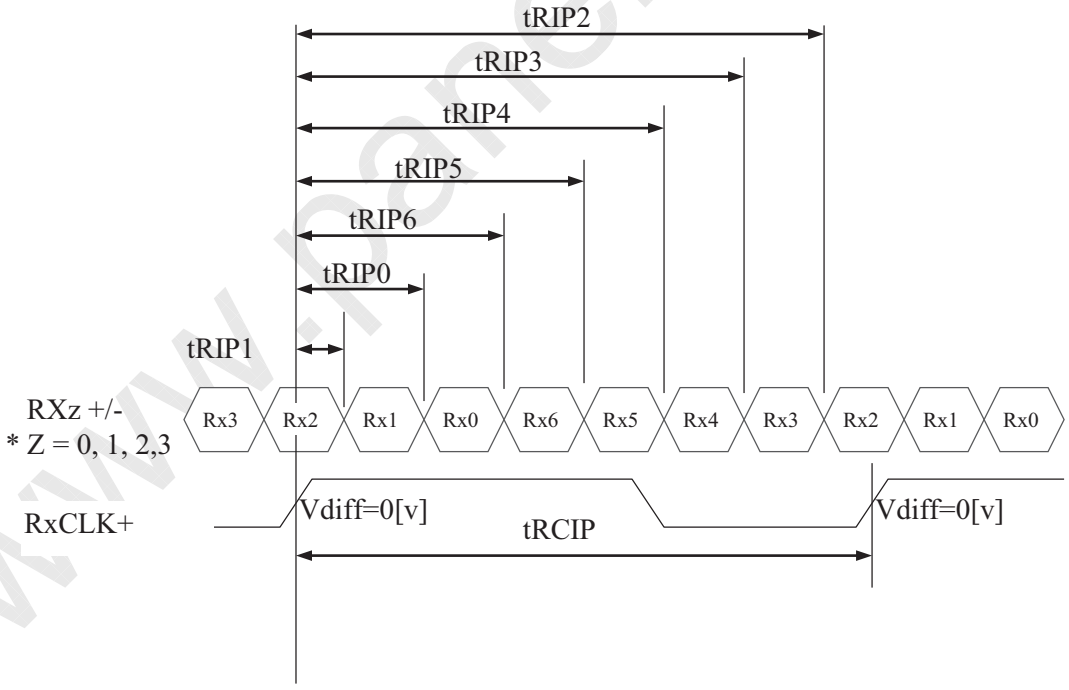
Item		Symbols	Min	Typ	Max	Unit
Clock	Frequency	1/Tc	45	54	67.5	MHz
	High Time	Tch	-	4/7Tc	-	
	Low Time	Tcl	-	3/7Tc	-	
Frame Period		Tv	1036	1066	1096	lines
			50	60	75	Hz
			20	16.7	13.3	ms
Vertical Display Period		Tvd	-	1024	-	lines
One line Scanning Period		Th	704	844	960	clocks
Horizontal Display Period		Thd	640	640	640	clocks

6.2 LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter is shown in Table 4.

<Table 4. LVDS Rx Interface Timing Specification>

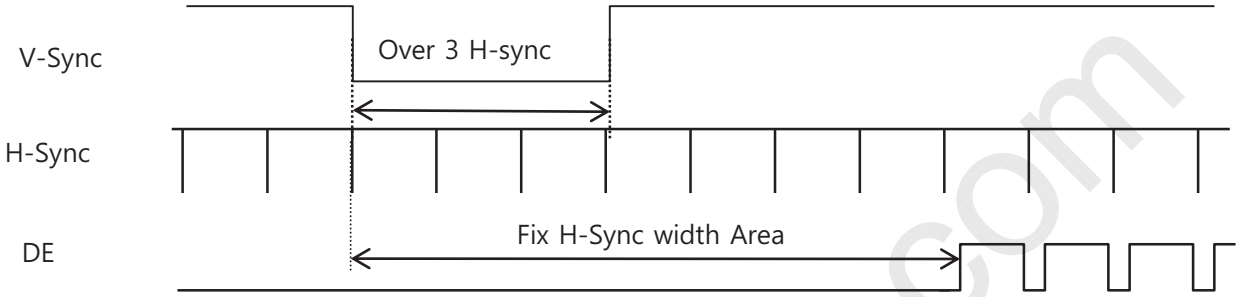
Item	Symbol	Min	Typ	Max	Unit	Remark
CLKIN Period	tRCIP	10.20	13.47	17.08	nsec	
Input Data 0	tRIP1	-0.4	0.0	+0.4	nsec	
Input Data 1	tRIP0	tRCIP/7-0.4	tRCIP/7	tRCIP/7+0.4	nsec	
Input Data 2	tRIP6	2 ×tRCIP/7-0.4	2 ×tRCIP/7	2 ×tRCIP/7+0.4	nsec	
Input Data 3	tRIP5	3 ×tRCIP/7-0.4	3 ×tRCIP/7	3 ×tRCIP/7+0.4	nsec	
Input Data 4	tRIP4	4 ×tRCIP/7-0.4	4 ×tRCIP/7	4 ×tRCIP/7+0.4	nsec	
Input Data 5	tRIP3	5 ×tRCIP/7-0.4	5 ×tRCIP/7	5 ×tRCIP/7+0.4	nsec	
Input Data 6	tRIP2	6 ×tRCIP/7-0.4	6 ×tRCIP/7	6 ×tRCIP/7+0.4	nsec	



* Vdiff = (RXz+)-(RXz-),…. ,(RXCLK+)-(RXCLK-)

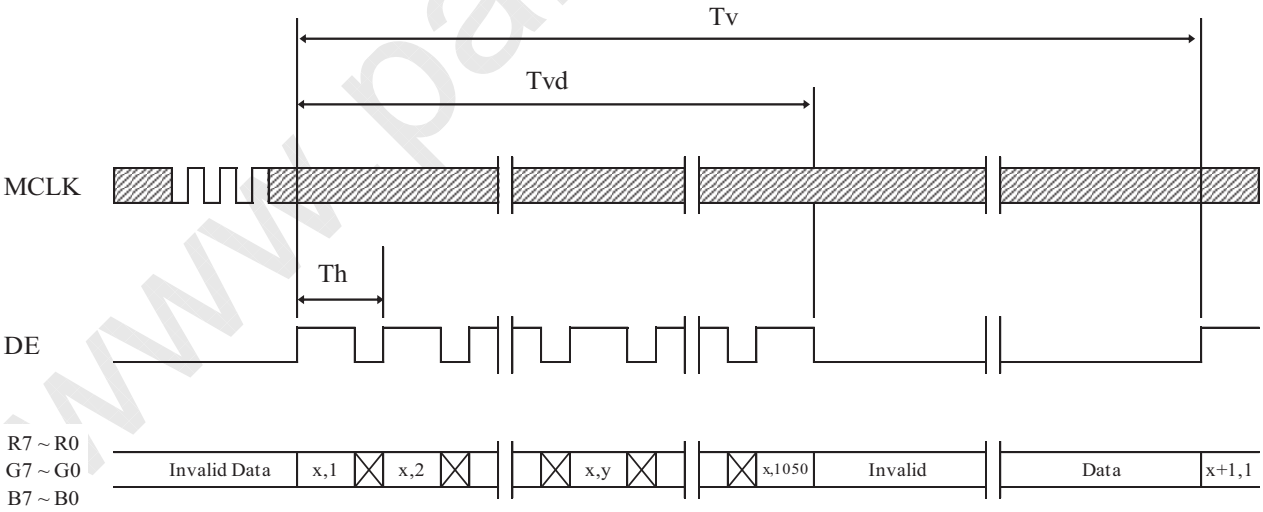
7.0 SIGNAL TIMING WAVEFORMS OF INTERFACE SIGNAL

7.1 Sync Timing Waveforms

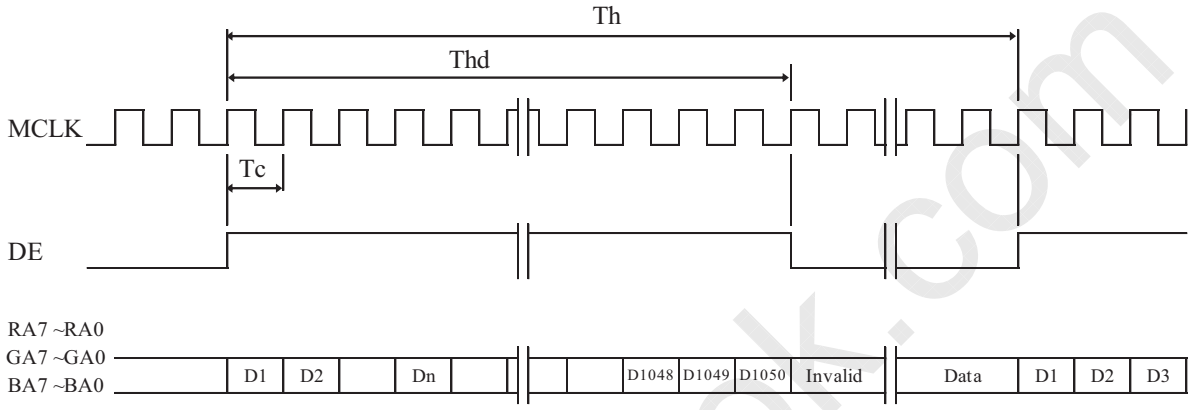


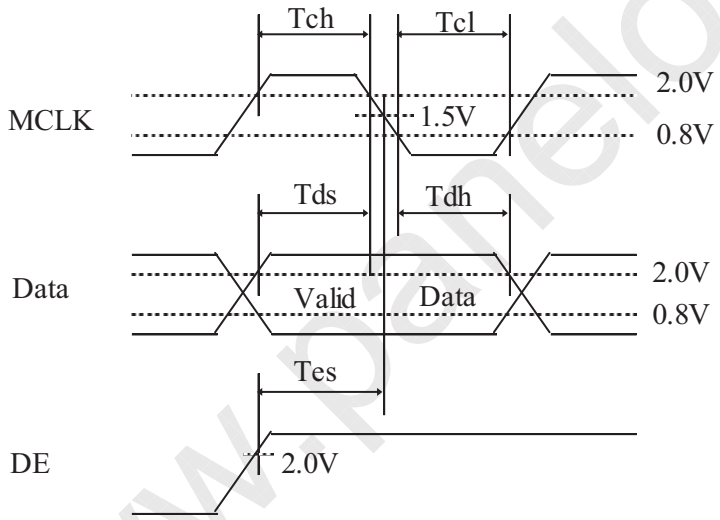
1) Need over 3 H-sync during V-Sync Low
2) Fix H-Sync width from V-Sync falling edge to first rising edge

7.2 Vertical Timing Waveforms



7.3 Horizontal Timing Waveforms



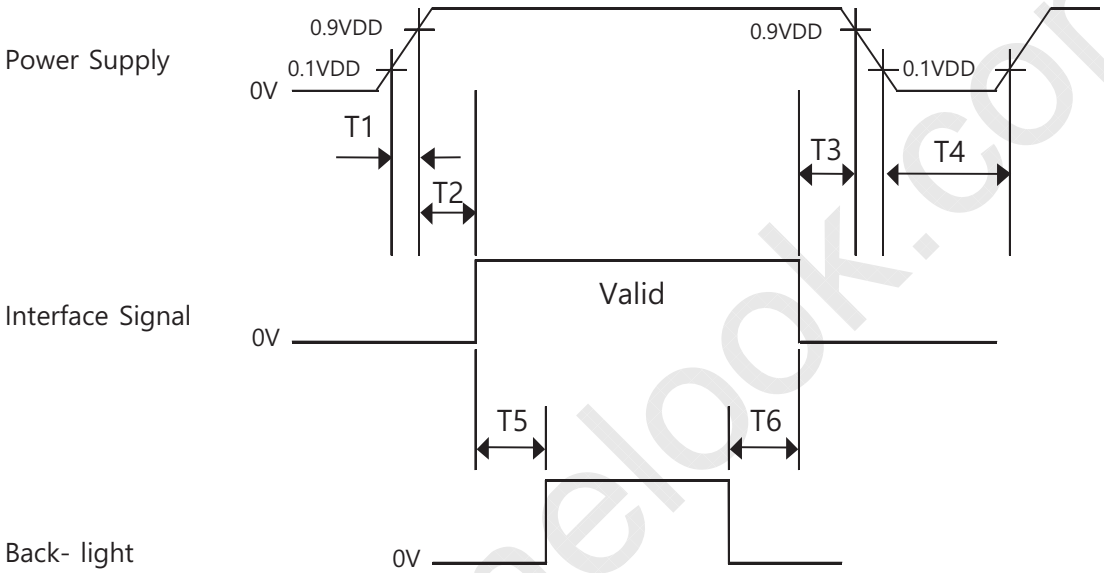


8.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

Color & Gray Scale		RED DATA								GREEN DATA								BLUE DATA							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of WHITE	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	▽	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

9.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



- 0.5 ms ≤ T1 ≤ 10 ms
- 0 ≤ T2 ≤ 50 ms
- 0 ≤ T3 ≤ 50 ms
- 1 sec ≤ T4
- 200 ms ≤ T5
- 200 ms ≤ T6

Notes:

1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
2. Do not keep the interface signal high impedance when power is on.
3. Back Light must be turn on after power for logic and interface signal are valid.

10.0 MECHANICAL CHARACTERISTICS

10.1 Dimensional Requirements

FIGURE 6 (located in Appendix) shows mechanical outlines for the model EV190E0M-N50. Other parameters are shown in Table 5.

<Table 5. Dimensional Parameters>

Parameter	Specification	Unit
Dimensional outline	396.0(H) × 324.0(V)× 9.9(D) typ.	mm
Weight	1690(typ)	gram
Active area	374.784(H) x 299.8272(V)	mm
Pixel pitch	0.0976(H) x 0.2928(V)	mm
Number of pixels	1280 (H)×1024 (V)	pixels
Back-light	Right edge side, 1-LED Lighting Bar type	

10.2 Mounting

See FIGURE 5. (shown in Appendix)

10.3 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

10.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

11.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below.

<Table 6. Reliability Test Parameters >

No	Test Items	Conditions	
1	High temperature storage test	Ta = 60 ℃, 240 hrs	
2	Low temperature storage test	Ta = -20 ℃, 240 hrs	
3	High temperature & high humidity operation test	Ta = 50 ℃, 80%RH, 240hrs	
4	High temperature operation test	Ta = 50 ℃, 240hrs	
5	Low temperature operation test	Ta = 0℃, 240hrs	
6	Thermal shock	Ta = -20 ℃ ↔ 60 ℃ (0.5 hr), 100 cycle	
7	Vibration test (non-operating)	Frequency Gravity / AMP Period	Random,10 ~ 300 Hz, 30 min/Axis 1.5 Grms X, Y, Z 30 min
8	Shock test (non-operating)	Gravity	50G
		Pulse width	11msec, sine wave
		Direction	±X, ±Y, ±Z Once for each
9	Electro-static discharge test (operating)	Air : 150 pF, 330Ω, 15 KV Contact : 150 pF, 330Ω, 8 KV	

12.0 HANDLING & CAUTIONS

(1) Cautions when taking out the module

- Pick the pouch only, when taking out module from a shipping package.

(2) Cautions for handling the module

- As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
- As the LCD panel and back - light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
- As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
- Do not pull the interface connector in or out while the LCD module is operating.
- Put the module display side down on a flat horizontal plane.
- Handle connectors and cables with care.

(3) Cautions for the operation

- When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
- Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.

(4) Cautions for the atmosphere

- Dew drop atmosphere should be avoided.
- Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.

(5) Cautions for the module characteristics

- Do not apply fixed pattern data signal to the LCD module at product aging.
- Applying fixed pattern for a long time may cause image sticking.

(6) Other cautions

- Do not disassemble and/or re-assemble LCD module.
- Do not re-adjust variable resistor or switch etc.
- When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

13.0 PRODUCT SERIAL NUMBER

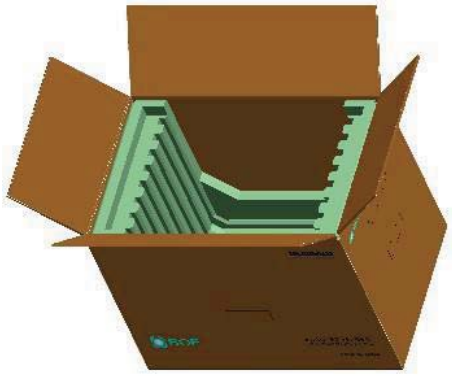
1	2	3	4	5	6	7
X X	X	X	X X	X	X X X X	X X X X X X X

1. Control Number
2. Rank / Grade
3. Line Classification
4. Year (2001 : 01, 2002 : 02, …)
5. Month (1,2,3, … , 9, X, Y, Z)
6. Internal Use
7. Serial Number

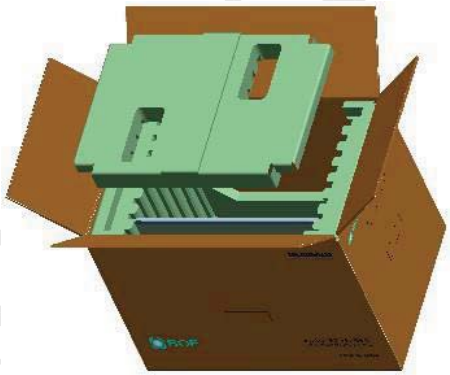
14.0 Packing

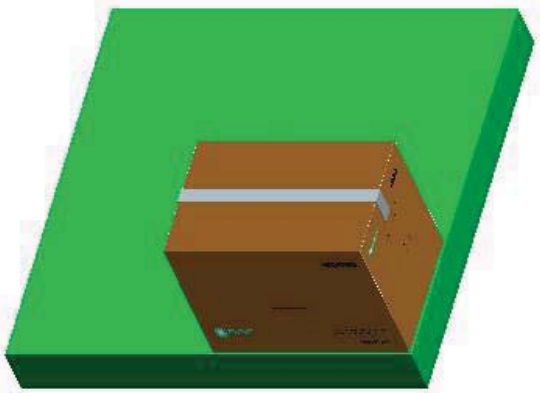
14.1 Packing Order

Put pad into the box

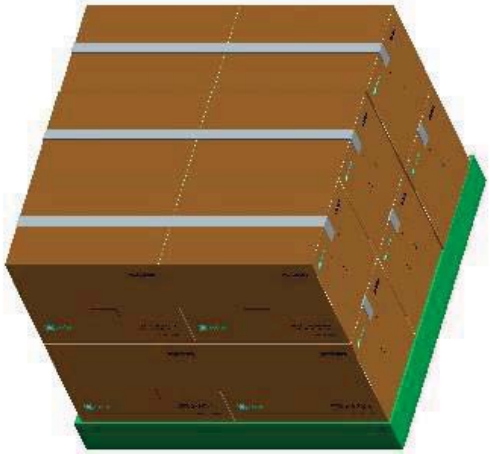


Place the modules bundled by packing bag in the box, 8pcs module per box, place a cover on the top of the box





After sealing the box, put the box on the pallet



12ea box per pallet

14.2 Packing Note

- Box Dimension : 521mm * 346mm * 403mm
- Package Quantity in one Box : 8 pcs

14.3 Box label

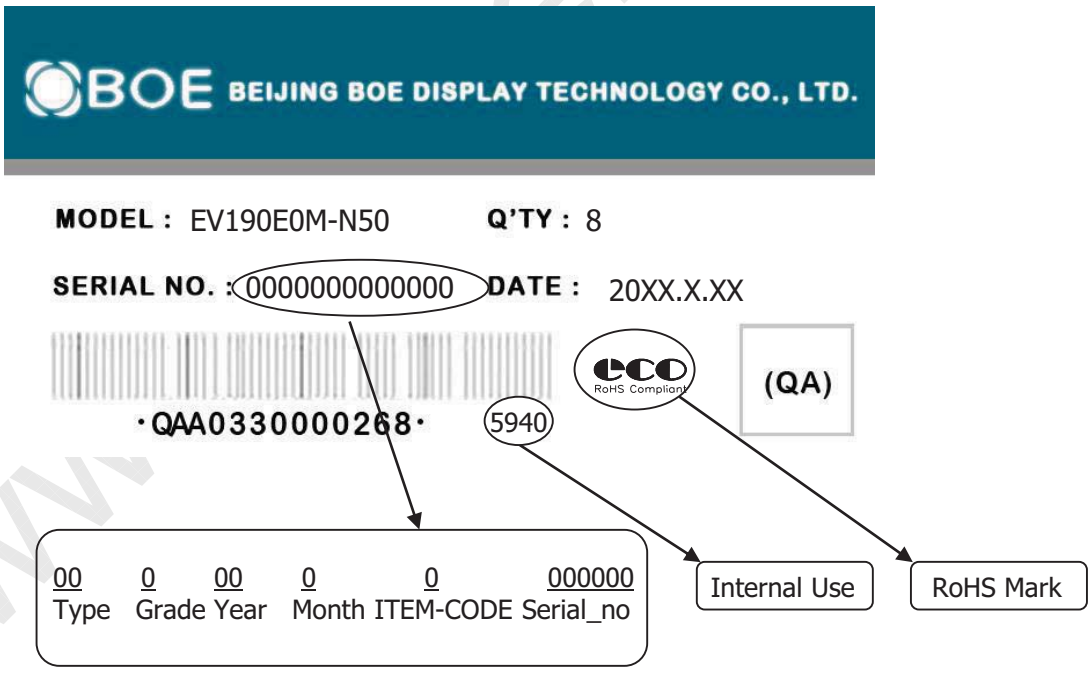
- Label Size : 108 mm (L) × 56 mm (W)
- Contents

  Model : EV190E0M-N50

  Q`ty : Module 8 Q`ty in one box

  Serial No. : Box Serial No. See next page for detail description.

  Date : Packing Date



15.0 APPENDIX

Figure 1. Measurement Set Up

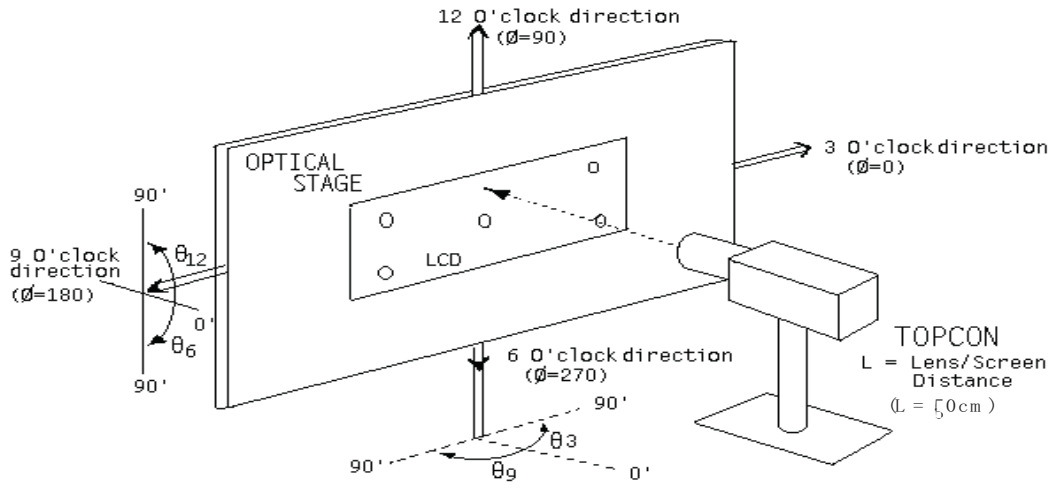


Figure 2. White Luminance and Uniformity Measurement Locations (9 points)

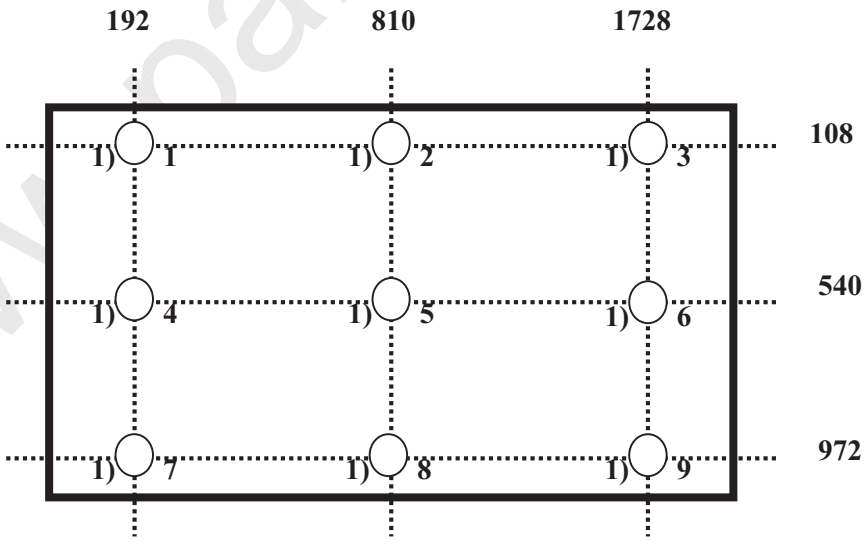


Figure 3. Response Time Testing

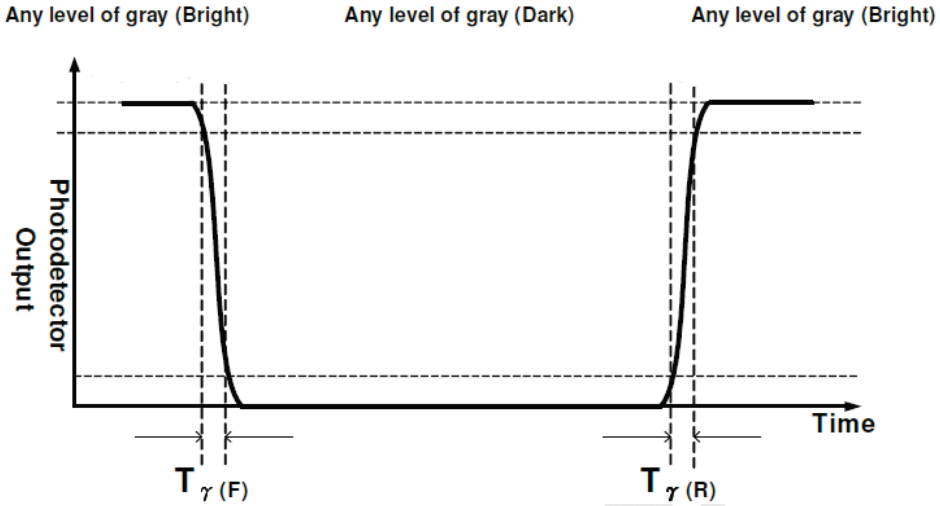
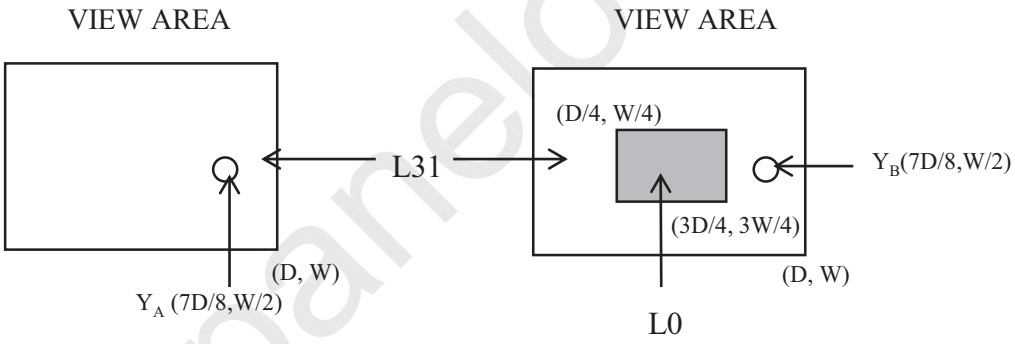


Figure 4. Cross Modulation Test Description



$$\text{Cross-Talk (\%)} = \left| \frac{Y_B - Y_A}{Y_A} \right| \times 100$$

Where: Y_A = Initial luminance of measured area (cd/m^2)
Y_B = Subsequent luminance of measured area (cd/m^2)
The location measured will be exactly the same in both patterns

Figure 5. TFT-LCD Module Outline Dimensions (Front view)

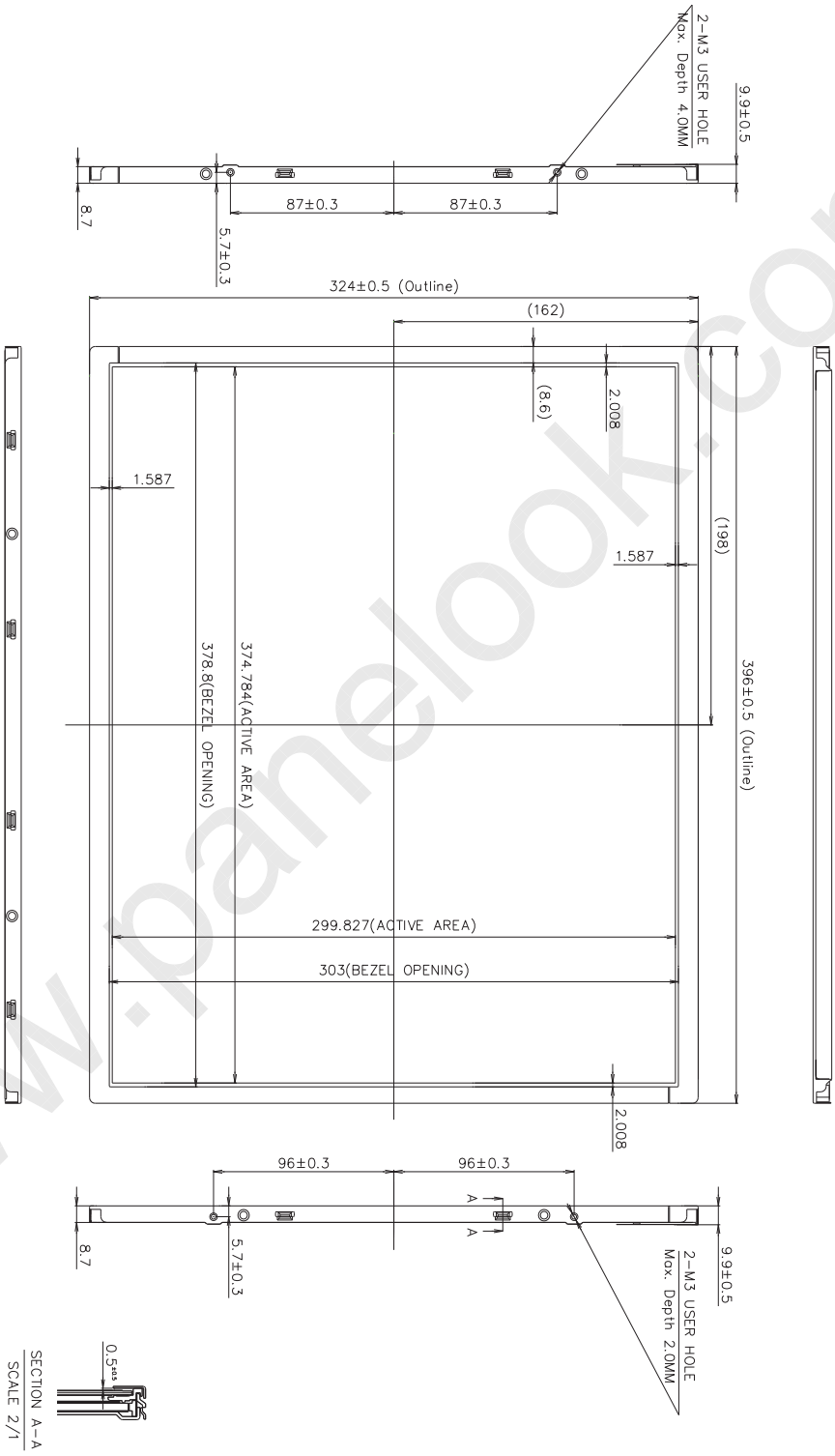


Figure 6. TFT-LCD Module Outline Dimensions (Rear view)