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# INNOLUX DISPLAY CORPORATION **SPECIFICATION**

**Customer:** 

Model Name:

SPEC NO .:

Date:

A070-92-TT-01 2009/01/12

AT070TN90

Version:

01

Preliminary Specification **Final Specification** 

#### For Customer's Acceptance

Approved by	Comment
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2009/02/12	2009/02/12	2009/02/11		

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### Contents

1.	General Specifications	1
2.	Pin Assignment	2
3.	Operation Specifications	5
	3.1. Absolute Maximum Rating	5
	3.1.1. Typical Operation Conditions	6
	3.1.2. Current Consumption	
	3.1.3. Backlight Driving Conditions	7
	3.2. Power Sequence	8
	3.3. Timing Characteristics	9
	3.3.1. AC Electrical Characteristics	9
	3.3.2. Data Input Format	
	3.3.3. Timing	
	Optical Specifications	
	Reliability Test Items	
6.	General Precautions	
	6.1. Safety	17
	6.2. Handling	17
	6.3. Static Electricity	
	6.4. Storage	17
	6.5. Cleaning	17
7.	Mechanical Drawing	18
8.	Package Drawing	19
	8.1 Packaging Material Table	19
	8.2 Packaging Quantity	19
	8.3 Packaging Drawing	20



SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:1/20

## **1. General Specifications**

No.	ltem	Specification	Remark
1	LCD size	7.0 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	-
3	Resolution	800 × 3(RGB) × 480	$\sim$
4	Display mode	Normally White, Transmissive	
5	Dot pitch	0.0642(W) × 0.1790(H) mm	
6	Active area	154.08(W) × 85.92(H) mm	
7	Module size	164.9(W) ×100.0(H) ×5.7(D) mm	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight power consumption	твр	
12	Panel power consumption	ТВD	
13	Weight	TBD	

Note 1: Refer to Mechanical Drawing.

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## INNOLUX

SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:2/20

## 2. Pin Assignment

#### **TFT LCD Panel Driving Section**

FPC Connector is used for the module electronics interface. The recommended model is FH12A-50S-0.5SH manufactured by Hirose.

Pin No.	Symbol	I/O	Function	Remark
1	V <sub>LED+</sub>	Р	Power for LED backlight (Anode)	
2	V <sub>LED+</sub>	Р	Power for LED backlight (Anode)	
3	V <sub>LED-</sub>	Р	Power for LED backlight (Cathode)	$\bigcirc$
4	V <sub>LED-</sub>	Р	Power for LED backlight (Cathode)	
5	GND	Р	Power ground	
6	V <sub>COM</sub>	I	Common voltage	
7	$DV_DD$	Р	Power for Digital Circuit	
8	MODE	I	DE/SYNC mode select	Note 1
9	DE	I	Data Input Enable	
10	VS	I	Vertical Sync Input	
11	HS	I	Horizontal Sync Input	
12	B7	I	Blue data(MSB)	
13	B6		Blue data	
14	B5	I	Blue data	
15	B4	1 🔶	Blue data	
16	В3	I	Blue data	
17	B2	I	Blue data	
18	B1	I	Blue data	Note 2
19	В0	I	Blue data(LSB)	Note 2
20	G7	I	Green data(MSB)	
21	G6	I	Green data	
22	G5	I	Green data	
23	G4	I	Green data	
24	G3	I	Green data	
25	G2	I	Green data	
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				Page:3/20
26	G1	I	Green data	Note 2
27	G0	I	Green data(LSB)	Note 2
28	R7	I	Red data(MSB)	
29	R6	I	Red data	
30	R5	I	Red data	
31	R4	I	Red data	
32	R3	I	Red data	
33	R2	I	Red data	
34	R1	I	Red data	Note 2
35	R0	I	Red data(LSB)	Note 2
36	GND	Р	Power Ground	
37	DCLK	I	Sample clock	Note 3
38	GND	Р	Power Ground	
39	L/R	I	Left / right selection	Note 4,5
40	U/D	I	Up/down selection	Note 4,5
41	V <sub>GH</sub>	Р	Gate ON Voltage	
42	V <sub>GL</sub>	Р	Gate OFF Voltage	
43	AV <sub>DD</sub>	Р	Power for Analog Circuit	
44	RESET	Ļ	Global reset pin.	Note 6
45	NC	-	No connection	
46	V <sub>COM</sub>		Common Voltage	
47	DITHB	I	Dithering function	Note 7
48	GND	Р	Power Ground	
49	NC	-	No connection	
50	NC	-	No connection	

I: input, O: output, P: Power

Note 1: DE/SYNC mode select. Normally pull high.

When select DE mode, MODE="1", VS and HS must pull high.

When select SYNC mode, MODE= "0", DE must be grounded.

- Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.
- Note 3: Data shall be latched at the falling edge of DCLK.

Page:4/20

SPEC NO.: A070-92-TT-01 Date :2009/01/12



Note 4: Selection of scanning mode

Setting of sca	n control input	Coording direction
U/D L/R		Scanning direction
GND	DV <sub>DD</sub>	Up to down, left to right
DV <sub>DD</sub>	GND	Down to up, right to left
GND	GND	Up to down, right to left
DV <sub>DD</sub> DV <sub>DD</sub>		Down to up, left to right

Note 5: Definition of scanning direction. Refer to the figure as below:



- Note 6: Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.
- Note 7: Dithering function enable control, normally pull high. When DITHB="1",Disable internal dithering function, When DITHB="0",Enable internal dithering function,



SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:5/20

## 3. Operation Specifications

#### 3.1. Absolute Maximum Ratings

	(Note 1)				
ltem	Symbol	Val	ues	Unit	Remark
nem	Symbol	Min.	Max.	Onit	Remark
	$DV_{DD}$	-0.3	5.0	V	
	AV <sub>DD</sub>	6.5	13.5	V	
Power voltage	$V_{GH}$	-0.3	40.0	V	
	V <sub>GL</sub>	-20.0	0.3	V	
	$V_{GH}$ - $V_{GL}$	-	40.0	V	
Operation Temperature	T <sub>OP</sub>	-20	70	°C	
Storage Temperature	T <sub>ST</sub>	-30	80	°C	
LED Reverse Voltage	VR	-	1.2	V	Each LED Note 2
LED Forward Current	lF	-	25	mA	Each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 2: VR Conditions: Zener Diode 20mA

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### INNOLUX

SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:6/20

#### 3.1.1. Typical Operation Conditions

	( Note 1)								
Item	Symbol		Values	Unit	Demoste				
	Symbol	Min.	Тур.	Max.	Onit	Remark			
	$DV_DD$	3.0	3.3	3.6	V	Note 2			
Device works are	$AV_{DD}$	(10.2)	(10.4)	(10.6)	V	$\frown$			
Power voltage	$V_{GH}$	(15.3)	(16.0)	(16.7)	V				
	$V_{GL}$	(-7.7)	(-7.0)	(-6.3)	V	~			
Input signal voltage	V <sub>COM</sub>	-	TBD	-	V				
Input logic high voltage	V <sub>IH</sub>	$0.7 \text{ DV}_{\text{DD}}$	_		V	Note 3			
Input logic low voltage	V <sub>IL</sub>	0	-	$0.3  \text{DV}_{\text{DD}}$	V	NOLE 5			

Note 1: Be sure to apply  $DV_{DD}$  and  $V_{GL}$  to the LCD first, and then apply  $V_{GH}$ .

Note 2: DV<sub>DD</sub> setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 3: DCLK,HS,VS,RESET,U/D, L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.

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### INNOLUX

#### 3.1.2. Current Consumption

SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:7/20

	Symbol		Values		Unit	Remark	
ltem	Symbol	Min.	Тур.	Max.	Onit		
	I <sub>GH</sub>	-	TBD	-	mA	V <sub>GH</sub> =17.0V	
Current for Driver	I <sub>GL</sub>	-	TBD	-	mA	V <sub>GL</sub> = -5.0V	
Current for Driver	IDV <sub>DD</sub>	-	TBD	-	mA	DV <sub>DD</sub> =3.3V	
	IAV <sub>DD</sub>	-	TBD	-	mA	AV <sub>DD</sub> =10.4V	

#### 3.1.3. Backlight Driving Conditions

ltem	Symbol		Values	Unit	Remark	
nem	Symbol	Min.	Тур.	Max.	Omt	Remark
Voltage for LED backlight	VL	(9.3)	(9.9)	(10.5)	V	Note 1
Current for LED backlight	ΙL	(170)	(180)	(200)	mA	
LED life time		20,000	-	-	Hr	Note 2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25 $^\circ\!C$  and I\_L =180mA.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and I<sub>L</sub> =180mA. The LED lifetime could be decreased if operating I<sub>L</sub> is lager than 180mA.

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### INNOLUX

SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:8/20

#### 3.2. Power Sequence

VGL

a. Power on:



B/L→Data→VGH→VGL→DV<sub>DD</sub>

|>5ms|

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>10ms

Note: Data include R0~R7, B0~B7, GO~G7, U/D, L/R, DCLK, HS,VS,DE.

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## INNOLUX

SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:9/20

### **3.3. Timing Characteristics**

#### 3.3.1. AC Electrical Characteristics

ltem	Symbol		Values		Unit	Remark	
nem		Min.	Тур.	Max.	Onit	Remark	
HS setup time	Thst	8	-	-	ns		
HS hold time	Thhd	8	-	-	ns	$\bigcirc$	
VS setup time	Tvst	8	-	-	ns		
VS hold time	Tvhd	8	-	-	ns		
Data setup time	Tdsu	8	-	-	ns		
Data hole time	Tdhd	8	F	-	ns		
DE setup time	Tesu	8		-	ns		
DE hole time	Tehd	8	-	-	ns		
DV <sub>DD</sub> Power On Slew rate	Tpor	$\mathcal{O}$	-	20	ms	From 0 to 90% DV <sub>DD</sub>	
RESET pulse width	TRst	1	-	-	ms		
DCLK cycle time	Tcoh	20	-	-	ns		
DCLK pulse duty	Tcwh	40	50	60	%		

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## INNOLUX

SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:10/20



Figure 3. 2 Vertical input timing diagram.

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### INNOLUX

SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:11/20

#### 3.3.3. Timing

ltem	Symbol		Values	Unit	Remark	
item i		Min.	Тур.	Max.	Onic	Kennark
Horizontal Display Area	thd	-	800	-	DCLK	
DCLK Frequency	fclk	26.4	33.3	46.8	MHz	
One Horizontal Line	th	862	1056	1200	DCLK	$\sim$
HS pulse width	thpw	1	-	40	DCLK	
HS Blanking	thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

ltom	Symbol		Values	11	Demerik	
ltem		Min.	Тур.	Max.	Unit	Remark
Vertical Display Area	tvd		480	-	TH	
VS period time	tv	510	525	650	TH	
VS pulse width	tvpw	1	-	20	TH	
VS Blanking	tvb	23	23	23	TH	
VS Front Porch	tvfp	7	22	147	TH	

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### INNOLUX

SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:12/20

## 4. Optical Specifications

ltem	Symbol Condition			Values		Unit	Remark	
nem	Symbol	Condition	Min.	Тур.	Max.	Onit	Remark	
	θ∟	Φ=180°(9 o'clock)	60	70	-		Note 1	
Viewing angle	$\theta_{R}$	Φ=0°(3 o'clock)	60	70	-			
(CR≥ 10)	θτ	Φ=90°(12 o'clock)	40	50	-	degree		
	θΒ	Φ=270°(6 o'clock)	60	70	-5			
Response time	T <sub>ON</sub>		-	10	20	msec	Note 3	
Response time	T <sub>OFF</sub>		-	15	30	msec	Note 3	
Contrast ratio	CR		400	500	-	-	Note 4	
Color chromaticity	W <sub>X</sub>	Normal θ=Φ=0°	0.26	0.31	0.36	-	Note 2	
	W <sub>Y</sub>		0.28	0.33	0.38	-	Note 5 Note 6	
Luminance	L		200	250	-	cd/m²	Note 6	
Luminance uniformity	Yu	C .	70	75	-	%	Note 7	

Test Conditions:

- 1.  $DV_{DD}$ =3.3V, I<sub>L</sub>=180mA (Backlight current), the ambient temperature is 25°C.
- 2. The test systems refer to Note 2.

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## INNOLUX

SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:13/20

Note 1: Definition of viewing angle range



Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)



Fig. 4-2 Optical measurement system setup

SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:14/20

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time ( $T_{ON}$ ) is the time between photo detector output intensity changed from 90% to 10%. And fall time ( $T_{OFF}$ ) is the time between photo detector output intensity changed from 10% to 90%.



Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

 $Contrast ratio (CR) = \frac{Luminance measured when LCD on the "White" state}{Luminance measured when LCD on the "Black" state}$ 

- Note 5: Definition of color chromaticity (CIE1931) Color coordinates measured at center point of LCD.
- Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is  $I_L$ =180mA.



SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:15/20

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (Yu) = 
$$\frac{B_{min}}{B_{max}}$$

L-----Active area length

W----- Active area width



Fig. 4-4 Definition of measuring points

**B**<sub>max</sub>: The measured maximum luminance of all measurement position. **B**<sub>min</sub>: The measured minimum luminance of all measurement position.

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SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:16/20

## 5. Reliability Test Items

(Note3)							
ltem	Test	Remark					
High Temperature Storage	Ta = 80℃	240hrs	Note 1 , Note 4				
Low Temperature Storage	Ta = -30℃	240hrs	Note 1 , Note 4				
High Temperature Operation	Ts = 70℃	240hrs	Note 2 · Note 4				
Low Temperature Operation	Ta = -20℃	240hrs	Note 1 , Note 4				
Operate at High Temperature and Humidity	+60℃, 90%RH	240hrs	Note 4				
Thermal Shock	-30℃/30 min ~ +80 cycles, Start with co with high temperate	Note 4					
Vibration Test	Frequency range:1 Stroke:1.5mm Sweep:10Hz~55Hz 2 hours for each di (6 hours for total)						
Mechanical Shock	100G 6ms,±X, ±Y, direction	±Z 3 times for each					
Package Vibration Test	Random Vibration 0.015G*G/Hz from from 200-500HZ 2 hours for each di (6 hours for total)	5-200HZ, -6dB/Octave					
Package Drop Test	Height:60 cm 1 corner, 3 edges,	6 surfaces					
Electro Static Discharge	± 2KV, Human Bo	ody Mode, 100pF/1500 $\Omega$					

Note 1: Ta is the ambient temperature of samples.

Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:17/20

## 6. General Precautions

#### 6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

#### 6.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.

2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.

3. To avoid contamination on the display surface, do not touch the module surface with bare hands.

4. Keep a space so that the LCD panels do not touch other components.

5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.

6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.

7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

#### 6.3. Static Electricity

1. Be sure to ground module before turning on power or operating module.

2. Do not apply voltage which exceeds the absolute maximum rating value.

#### 6.4. Storage

1. Store the module in a dark room where must keep at  $25\pm10^\circ$ C and 65%RH or less.

2. Do not store the module in surroundings containing organic solvent or corrosive

gas.

3. Store the module in an anti-electrostatic container or bag.

#### 6.5. Cleaning

- 1. Do not wipe the polarizer with dry cloth. It might cause scratch.
- 2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

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SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:18/20

## 7. Mechanical Drawing



SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:19/20

## 8. Package Drawing

#### 8.1. Packaging Material Table

No.	ltem	Model (Material)	Dimensions(mm) (kg)		Quantity	Remark
1	LCM Module	AT070TN92	164.9 × 100.0 × 5.7	TBD	50pcs	
2	Partition	BC Corrugated paper	512 × 349 × 226	1.466	1set	
3	Corrugated Paper	B Corrugated paper	510 × 350	0.071	4pcs	
4	Corrugated Bar	B Corrugated paper	512 × 11 × 3	0.046	4pcs	
5	Dust-Proof Bag	PE	700 × 530	0.048	1pcs	
6	A/S Bag	PE	180 × 133 × 0.2	0.002	50pcs	
7	Carton	Corrugated paper	530 × 355 × 255	1.100	1pcs	
8	Total weight		твр			

#### 8.1 Packaging Quantity

Total LCM quantity in Carton: no. of Partition 2 Rows × quantity per Row 25 = 50

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## INNOLUX

SPEC NO.: A070-92-TT-01 Date :2009/01/12 Page:20/20

